

PATENTS
LT-140

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Steven D. Roach
Application No. : 10/003,096 Confirmation No.: 5186
Filed : December 6, 2001
For : CIRCUITRY AND METHODS FOR IMPROVING
THE PERFORMANCE OF A LIGHT EMITTING
ELEMENT
Group Art Unit : 2817
Examiner : Not yet assigned

Hon. Commissioner
for Patents
Washington, D.C. 20231

June 25, 2002
New York, New York

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. ** 1.56 and 1.97,
applicants wish to call the attention of the Examiner to
the following document:

U.S. Patents

Evel	3,573,644	April 6, 1971
Roach	5,121,075	June 9, 1992

Other Documents

Addis, John, "Three technologies on one chip make
a broadband amplifier", Electronics The International
Magazine of Electronics Technology, June 5, 1972, pp. 103-
107.

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TECHNOLOGY CENTER 2800

Ahuja, B.K., "Implementation of Active Distributed RC Anti-Aliasing/Smoothing Filters", IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 6, pp. 339-342.

AMCC Product Brief 1.0/1.25GBPSVCSELDRIER, May 10, 1999, pp. 1-3.

Ikalainen, Pertti, K. "An RLC Matching Network and Application in 1-20 GHz Monolithic Amplifier", IEEE MTT-S International Microwave Symposium Digest, Vol. I, 1989, pp. 1115-1118.

Khoury, John, M., "Synthesis of Arbitrary Rational Transfer Functions Using Uniform Distributed RC Active Circuits", IEEE Transactions on Circuits and Systems, Vol. 37, No. 4, April 1990, pp. 464-472.

Khoury, John, M., "On the Design of Constant Settling Time AGC Circuits", IEEE Transactions on Circuits and Systems, Vol. 45, No. 3, March 1998, pp. 283-294.

Sackinger, Eduard, et al., "A 3GHz, 32dB CMOS Limiting Amplifier for SONET OC-48 Receivers", IEEE International Solid-State Circuits Conference, 2000, pg. 158.

SUMMIT Microelectronics, Inc. "Dual Loop Laser Diode Adaptive Power Controller with Look Up Table" (SML2108), October 3, 2001, pp. 1-21.

Swartz, R.G. et al., "An Integrated Circuit for Multiplexing and Driving Injection Lasers", IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 4, August 1982, pp. 753-760.

A copy of this document is enclosed herewith. It is respectfully requested that the document be: (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent which may issue on this application. Applicant requests that a copy of Form PTO-1449 (submitted in

duplicate herewith), as considered and initialed by the Examiner, be returned with the next communication.

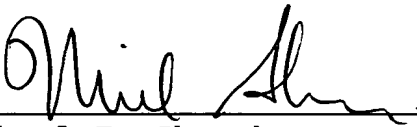
Applicant believes that no fee is due at this time. However, the Director is hereby authorized to charge any fee, or credit any overpayment, that may be due in connection with this Information Disclosure Statement, to Deposit Account No. 06-1075. A duplicate copy of this Information Disclosure Statement is enclosed herewith.

An early and favorable action is respectfully requested.

Respectfully submitted,

I hereby certify that this
correspondence is being
deposited with the U.S.
Postal Service as First
Class Mail in an envelope
addressed to:
Commissioner for Patents, P.O. Box 2327
ARLINGTON, VA 22202, on

June 25, 2002
William Bailey
Name of person signing
W.D. Bailey
Signature of person signing



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